

***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

Applicant: Keiichiro KATA et al.

Title: PROCESS FOR MANUFACTURING  
SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR WAFER

Patent No.: RE39,603

Reissue Date: 05/01/2007

Appl. No.: 10/645,782

Filing Date: 08/22/2003

Examiner: Mark V. Prenty

Art Unit: 2822

Confirmation 8077  
Number:

**PETITION TO CORRECT NAMES OF ASSIGNEES ON AN ISSUED PATENT**  
**UNDER 37 C.F.R. § 1.183**

Mail Stop Petitions  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants respectfully request that 37 C.F.R. §3.81(a) be waived to permit the correct names of the assignees to be provided after issuance for the above-identified patent.

Applicants hereby state that the failure to include the correct assignee names on Form PTOL-85B was inadvertent and enclose herewith a copy of the Notice of Recordation of Assignment Document and a Request for Certificate of Correction, including Form PTO/SB/44 indicating the corrections to be made.

A credit card payment in the amount of \$130.00 is enclosed for the amount stated in 37 C.F.R. § 1.17(h) to cover the fee for this petition.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by the credit card payment form being unsigned, providing incorrect information resulting in a rejected credit card transaction, or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date February 8, 2008

FOLEY & LARDNER LLP  
Customer Number: 22428  
Telephone: (202) 672-5535  
Facsimile: (202) 672-5399

By Phillip J. Articola

Phillip J. Articola  
Attorney for Applicant  
Registration No. 38,819

**Patent Assignment Abstract of Title**

**NOTE: Results display only for issued patents and published applications.  
For pending or abandoned applications please consult USPTO staff.**

**Total Assignments: 2****Patent #:** 5844304**Issue Dt:** 12/01/1998**Application #:** 08533207**Filing Dt:** 09/25/1995**Inventor:** \*\*\* PALM SYSTEM IS NOT AVAILABLE \*\*\***Title:** PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR WAFER**Assignment: 1****Reel/Frame:** 007797/0511**Recorded:** 09/25/1995**Pages:** 3**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** KATA, KEIICHIRO**Exec Dt:** 09/12/1995CHIKAKI, SHINICHI**Exec Dt:** 09/12/1995**Assignee:** NEC CORPORATION7-1, SHIBA 5-CHOME, MINATO-KU  
TOKYO, JAPAN**Correspondent:** WHITHAM, CURTIS, WHITHAM & MCGINN  
SEAN M. MCGINN, ESQ.  
RESTON INTERNATIONAL CENTER  
11800 SUNRISE VALLEY DRIVE, SUITE 900  
RESTON, VA 22091**Assignment: 2****Reel/Frame:** 013798/0626**Recorded:** 02/25/2003**Pages:** 30**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignor:** NEC CORPORATION**Exec Dt:** 11/01/2002**Assignee:** NEC ELECTRONICS CORPORATION1753 SHIMONMUMAMBE, NAKAHARA-KU  
KAWASAKI, KANAGAWA, JAPAN 211-8668**Correspondent:** SUGHRUE MION, PLLC  
2100 PENNSYLVANIA AVENUE, N.W.  
WASHINGTON, DC 20037